Honorable Commissioner of
Patents and Trademarks,
Washington, D.C. 20231

RESPONSE TO OFFICE ACTION UNDER 37 CFR § 1.111

In response to an Office Action mailed October 13, 1999 regarding the above
patent application, the applicant respectfully submits the following response.

AMENDMENT

Please amend the following claims as indicated:

1. (Amended) A method for configuring multiple displays with a computing system, the
method comprises the steps of:

(a) receiving, a coupling controller, display preferences regarding at least one of the
multiple displays;

(b) determining, the coupling controller, whether the display preferences can be
fulfilled in observance of at least one of: configuration properties of the at least one of
the multiple displays and configuration properties of the computing system; and

(c)   when the display preferences can be fulfilled, configuring, <u>the coupling controller,</u> the computing system and the at least one of the multiple displays in accordance with the display preferences.

14.   A video graphics processing circuit comprises:

a plurality of display controllers <u>included on a single video graphics card</u>;

memory, wherein at least a portion of the memory is screen memory;

coupling module operably coupled to the plurality of displays and the screen memory; and

a coupling controller operably coupled to receive display preferences and to determine whether the display preferences can be fulfilled in observance of configuration properties, wherein, when the display preferences can be fulfilled, the coupling controller provides configuration requirements to the coupling module, wherein the coupling module, based on the configuration requirements, operably couples at least one of the plurality of display controllers with at least a portion of the screen memory and with at least one display, and wherein the at least one of the plurality of display controllers retrieves display data from the at least a portion of the screen memory and provides the display data to the at least one display.

24. (Amended) A video graphics processing circuit comprises:

a processing unit; and

memory operably coupled to the processing unit, wherein the memory stores programming instructions that, when executed by processing unit, cause <u>a coupling</u>

controller of the processing unit to (a) receive display preferences regarding at least one of a multiple displays; (b) determine whether the display preferences can be fulfilled in observance of at least one of: configuration properties of the at least one of the multiple displays and configuration properties of a computing system; and (c) configure the computing system and the at least one of the multiple displays in accordance with the display preferences when the display preferences can be fulfilled.

33. (Amended) A digital storage medium for storing programming instructions that, when executed by a processing unit, cause the processing unit to configure multiple displays associated with a computing system, the digital storage medium comprises:

first means for storing programming instructions that cause a coupling controller of the processing unit to receive display preferences regarding at least one of the multiple displays;

second means for storing programming instructions that cause the coupling controller of the processing unit to determine whether the display preferences can be fulfilled in observance of at least one of: configuration properties of the at least one of the multiple displays and configuration properties of the computing system; and

third means for storing programming instructions that cause the coupling controller of the processing unit to configure the computing system and the at least one of the multiple displays in accordance with the display preferences when the display preferences can be fulfilled.

REMARKS